操作系统笔记

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1. 导论

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L1 cache reference	0.5 ns
Branch mispredict	5 ns
L2 cache reference	7 ns
Mutex lock/unlock	25 ns
Main memory reference	100 ns
Compress 1K bytes with Zippy	30,00 ns
Send 2K bytes over 1 Gbps	20,000 ns
network	
Read 1 MB sequentially from	250,000 ns
memory	
Round trip within same	500,000 ns
datacenter	
Disk seek	10,000,000 ns
Read 1 MB sequentially from disk	20,100,000 ns

表 1 Numbers Everyone Should Know